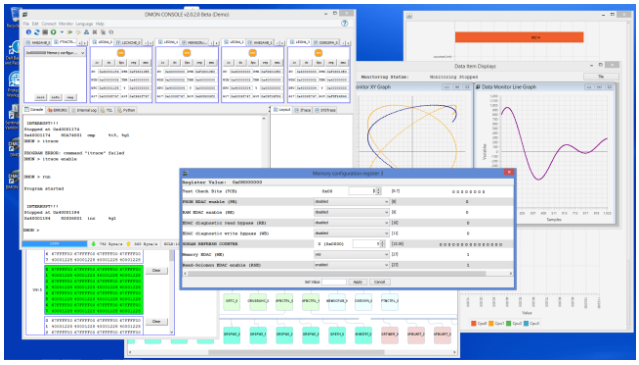
DMON2

Debug Monitor tool for System-on-Chip

PRODUCT DESCRIPTION

DMON2 is a software tool to improve developer productivity by speeding up the debug of embedded software applications on system-on-chips (SOCs) with one or more SPARC/ARM processor cores. It provides the following facilities:



- GUI with unique visualisation of SOC activity
- Drill-down for each IP core detected to register and bit level
- Load and execution of multicore embedded applications
- Breakpoint, watchpoint and disassembly support
- Support for serial, Ethernet, USB and JTAG debug links
- Support for user-defined IP and commands
- Integration with IDEs as GDB remote target
- Support for SPARC V8 and ARM processor architectures
- Runs on Windows, Linux, & Cygwin
- Remote access client/server support
- Support for TCL & Python scripting
- Statistics module with graph/store options for target data
- Developed in cooperation with European Space Agency (ESA)

FEATURES & BENEFITS

Details

DMON2 is a software tool for embedded system developers to improve their productivity. It enables them to debug applications in a faster and more efficient manner. Test & Debug operations account for 25% of developers' time and developers consider debug tools to be their most important tools (UBM survey 2013). DMON2 provides unique supports for efficient development and debugging of system-on-chip solutions where a single chip contains multiple blocks each performing a different function. With upwards of 55% of embedded projects running late (UBM) the demand by management for DMON2 is obvious.

DMON2 is targeted at debug of embedded applications on system-on-chip (SOC) devices. On-chip processor architectures currently supported include SPARC and ARM. DMON2 communicates with the target SOC over a dedicated communications link allowing debug without modification of target code or special debug libraries. As DMON2 is a Java program it runs on multiple platforms including Windows, Linux and Cygwin.

DMON2 provides a unique GUI interface making it easy for developers to track activity in a multi-processor environment. By clicking on-screen IP blocks the developer can quickly drill down to examine registers and trace data with extensive help on bit definitions and functions for each register.

The primary function of a debug monitor is testing of application code and DMON2 provides a test framework to allow the automation and scheduling of complex test scenarios. Its support for TCL and Python facilitate this process and allow users to build on legacy scripts used by in-house test processes. DMON2 can be used as a standalone debug tool or integrated with the IDE as a GDB remote target.

It provides offline storage of trace information for later analysis. DMON2 includes a framework to enable developers support proprietary hardware IP and to add commands to manipulate this IP.

Where expensive target hardware is in short-supply or where remote teams are developing without on-site hardware DMON2 allows remote debugging to be carried out on centrally-controlled hardware resources.

DMON2 has been developed in close cooperation with the European Space Agency.

For further details email or call:

Sales Department, O.C.E. Technology Ltd.,
NovaUCD, Belfield Innovation Park,
Belfield, Dublin 4,
D04 X8W9, Ireland.

Phone: +353 1 716 3530
Email: sales@ocetechnology.com

Distributor: